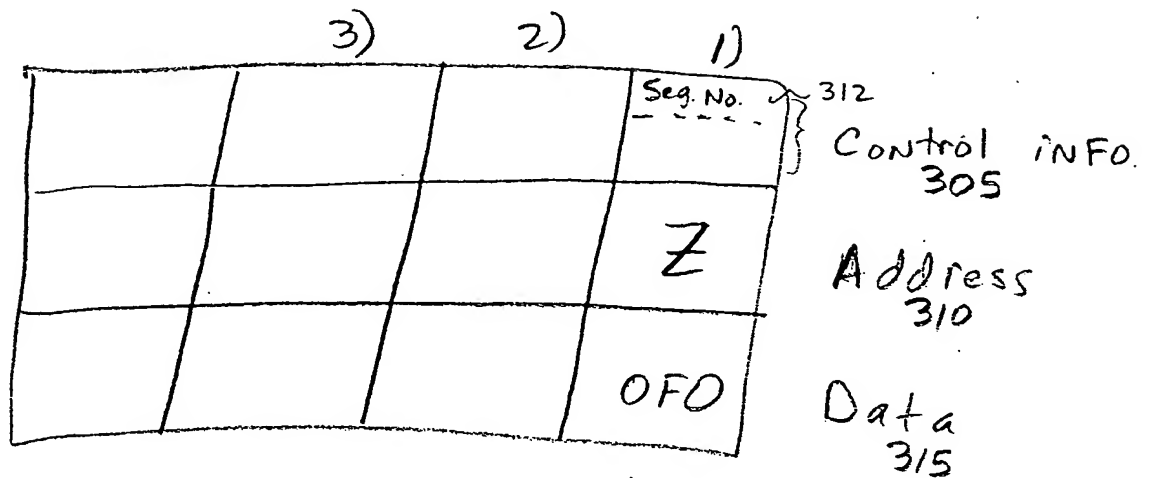


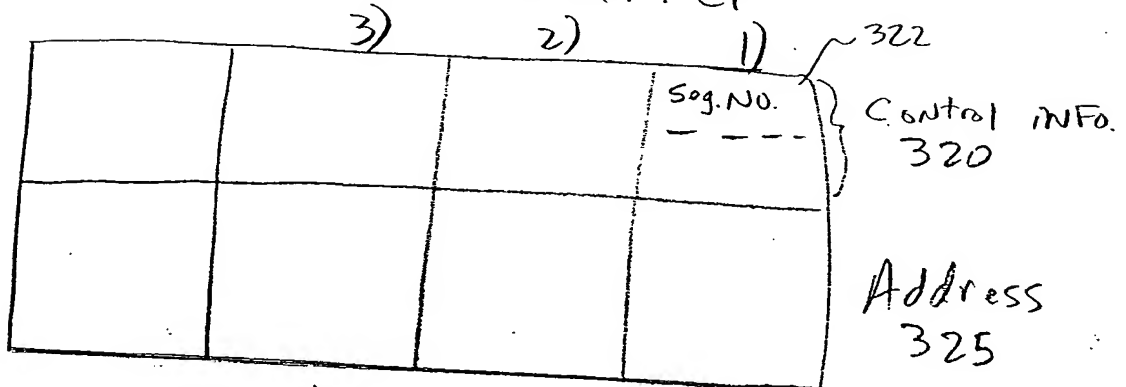
**Fig. 2**

# Store Buffer



143

# Load Buffer



141

Fig. 3

	Operation	SRC	Dest
1)	Store	R4,	[R1]
2)	Load	[X],	R2

Reg.	Correct Value	Incorrect Value
R <sub>1</sub>	X	Y

### Lo Cache System 120

Addr.	Data
X	F000

Fig. 4

# Store Buffer

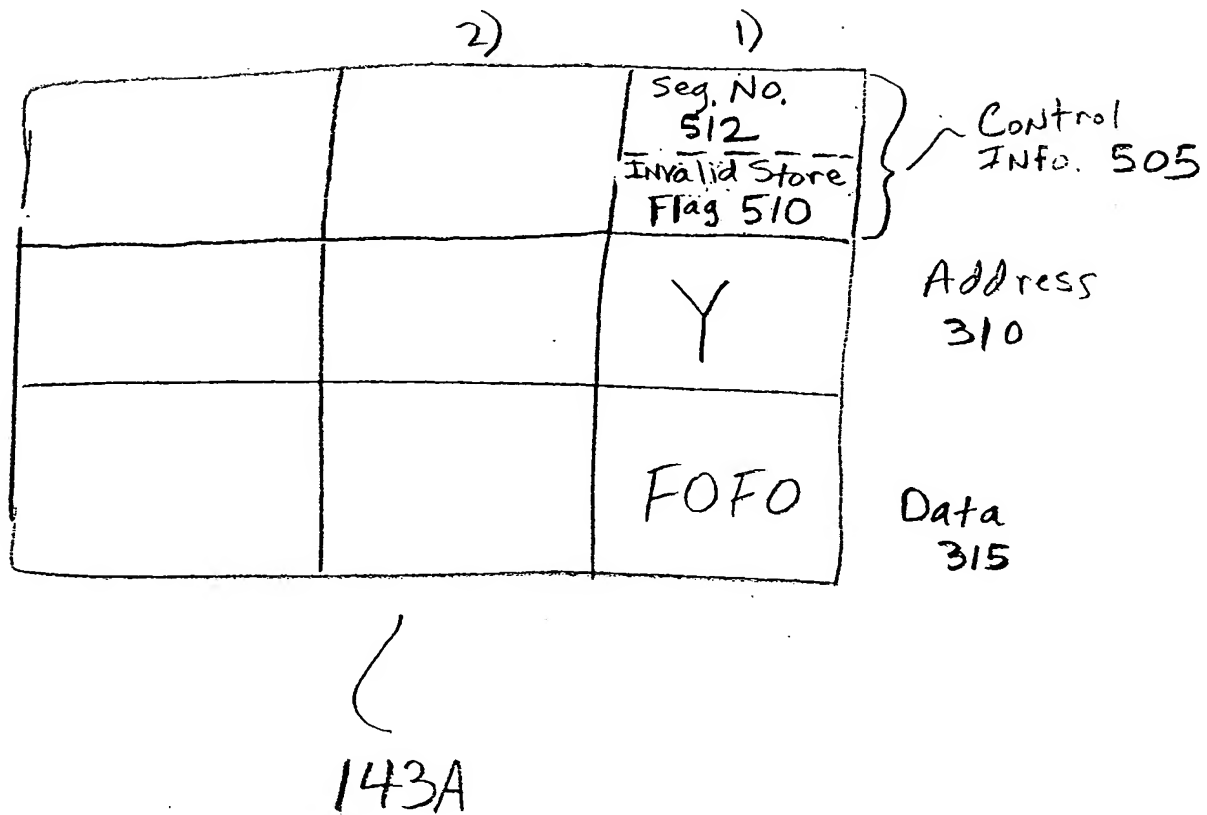


Fig. 5

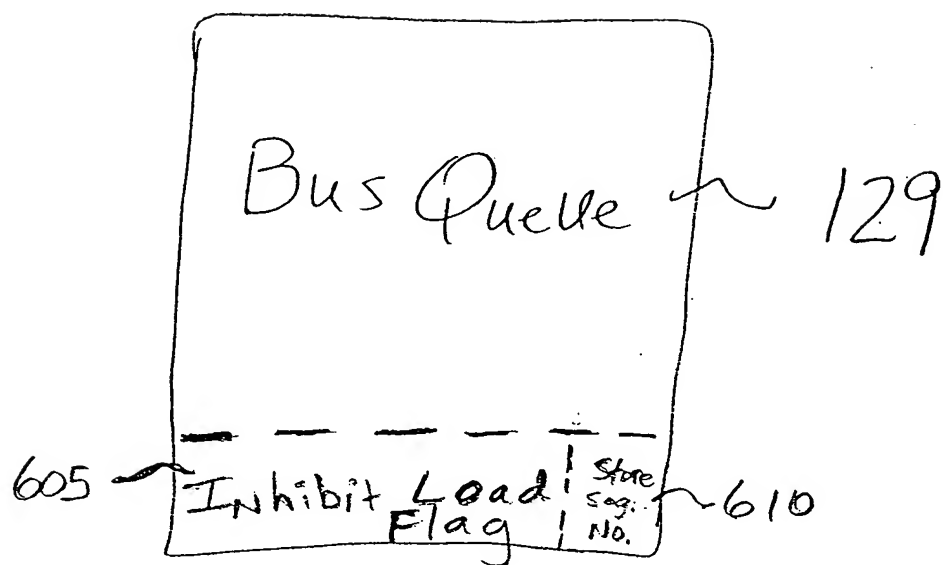


Fig. 6

# Store Buffer

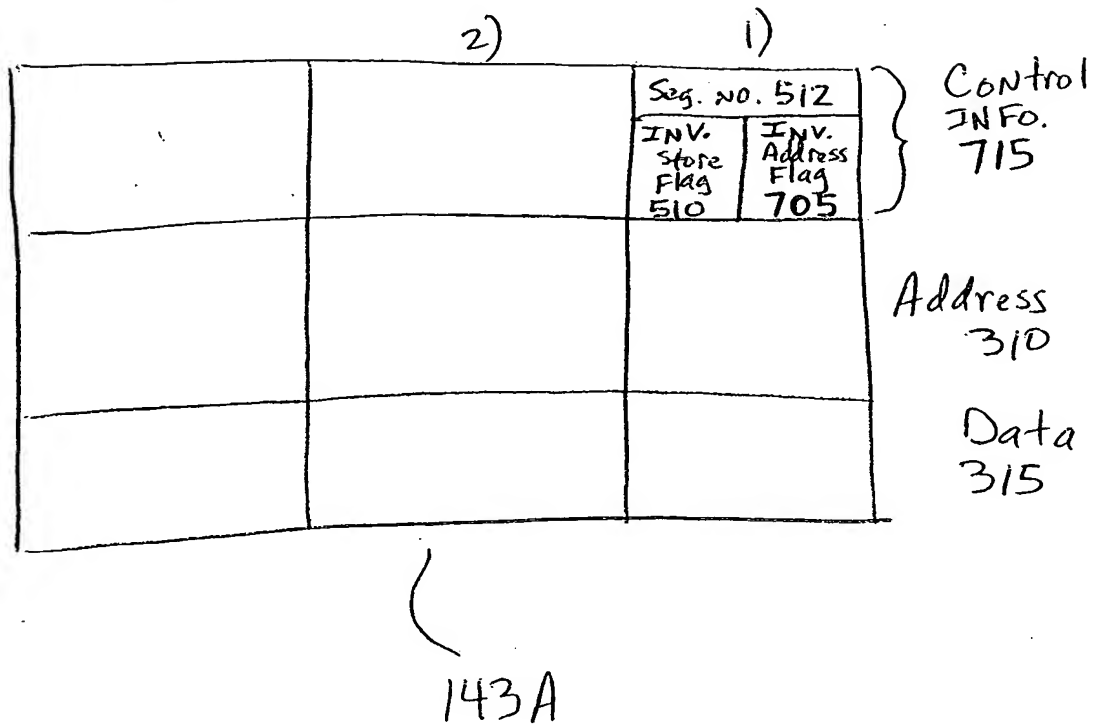


Fig. 7